

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/769,249	01/30/2004	Pankaj Sharma	200314191-1	8147		
22879	7590 12/23/2005		EXAM	EXAMINER		
	PACKARD COMPAN	CHANG, YEAN HSI				
	400, 3404 E. HARMON'	ART UNIT	PAPER NUMBER			
INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400			2835			

DATE MAILED: 12/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

EX

			Application	No.	Applicant(s)				
Office Action Summary		10/769,249		SHARMA ET AL.					
		Γ	Examiner		Art Unit				
			Yean-Hsi Ch	ang	2835				
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
1)⊠	Responsive to communication(s) file	ed on 30 Jar	nuarv 2004.						
·	This action is FINAL . 2b)⊠ This action is non-final.								
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is								
- / (closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
Dispositi	on of Claims								
4)⊠	Claim(s) 1-12 is/are pending in the	application.			•				
-	4a) Of the above claim(s) is/are withdrawn from consideration.								
	☐ Claim(s) is/are allowed.								
·	☐ Claim(s) is/are dilewed. ☐ Claim(s) <u>1,2 and 4-8</u> is/are rejected.								
·	Claim(s) 3 and 9-12 is/are objected		•	•					
·	Claim(s) are subject to restri		election req	uirement.					
Applicati	on Papers								
9)□	The specification is objected to by the	ne Examiner.							
9) The specification is objected to by the Examiner. 10) The drawing(s) filed on <u>30 January 2004</u> is/are: a) accepted or b) objected to by the Examiner.									
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).									
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).									
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.									
, —	rinder 35 U.S.C. § 119	•							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).									
a)[a) All b) Some * c) None of:								
	1. Certified copies of the priority documents have been received.								
	2. Certified copies of the priority documents have been received in Application No								
	3. Copies of the certified copies of the priority documents have been received in this National Stage								
application from the International Bureau (PCT Rule 17.2(a)).									
* See the attached detailed Office action for a list of the certified copies not received.									
A 44	44-5								
Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)									
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)	4	Paper No(s)/Mail Da					
3) Inform	nation Disclosure Statement(s) (PTO-1449 o r No(s)/Mail Date		_) Notice of Informal P) Other:	atent Application (PT	O-152)			

Application/Control Number: 10/769,249 Page 2

Art Unit: 2835

DETAILED ACTION

Claim Objections

1. Claims 1, 5-6 and 9-10 are objected to because of the following informalities:

The second and the third "a low-voltage power button" in claim 1 should not use "a" as an article if they refer the same subject matter; and the "the current power state" in claims 5-6 and 9-10 lack antecedent bases. Appropriate correction is required.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-2 and 4-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yerazunis et al. (US 6,477,588 B1) in view of Chen (US 6,525,926 B1).

Yerazunis teaches a computer system (fig. 1) having a local or remote low-voltage power button (107), comprising: a main unit (150) comprising an enclosure (not labeled) and circuitry (not shown) for sensing activity in the low-voltage power button, and a first socket or plug (161) accessible from the exterior of the enclosure and electrically coupled to the circuitry for sensing activity in the low-voltage power button;

Art Unit: 2835

wherein the first socket or plug is adapted to removably mate with a corresponding socket or plug (102) of either a low-voltage power button or a low-voltage power button extender assembly (100) (claim 1); and wherein the low-voltage power button comprises a switch (obvious feature not shown) integrated in fixed relationship with and electrically coupled to a second socket or plug (102), the second socket or plug adapted to removably mate with the first socket or plug (claim 2).

Yerazunis fails to teach a light operable to indicate the current power state of the computer system wherein the light is an LED located on a housing of the switch, or integrated within the switch.

Chen teaches a computer system (fig. 1) comprising: an LED power indicator 98 located and integrated in a housing 72 at a front surface of a power switch 92 as shown in figs. 2 and 4. It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Yerazunis with the switch and the indicator taught by Chen for indicating the power status of the system.

Allowable Subject Matter

- 4. Claims 3 and 9-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 5. The following is a statement of reasons for the indication of allowable subject matter: The best prior art of record, Yerazunis et al. (US 6,477,588 B1), and Chen (US

Application/Control Number: 10/769,249 Page 4

Art Unit: 2835

6,525,926 B1), taken alone or in combination, fails to teach or fairly suggest a computer system comprising at least: a low-voltage power button extender assembly comprises a switch electrically coupled to a second socket or plug by a flexible cable, the second socket or plug adapted to removably mate with the first socket or plug accessible from an exterior of an enclosure of the computer system and electrically coupled to a circuitry for sensing activity in a low-voltage power button of the low-voltage power button extender assembly as set forth in claim 3. Claims 9-12 are dependent claims from claim 3.

Correspondence

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yean-Hsi Chang whose telephone number is (571) 272-2038. The examiner can normally be reached on 07:30 - 16:00, Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the Art Unit phone number is (571) 272-2800, ext. 35. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system,

Application/Control Number: 10/769,249 Page 5

Art Unit: 2835

see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-8558.

Yean-Hsi Chang Primary Examiner Art Unit: 2835 December 21, 2005

> YEAN-HSI CHANG PRIMARY EXAMINED